




652	702
Class	Subclass
ISSUE CLASSIFICATION	

PATENT N°

## U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
 SCANNED  O.A. 	JUL 22 2003

APPLICATION NO. 09/874631	CONT/PRIOR F	CLASS <del>438</del> 257	SUBCLASS 701	ART UNIT <del>2012</del> 2815	EXAMINER S. Clark
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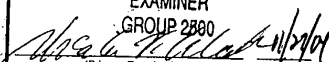

**TITLE**

Flexible ball grid array chip scale packages and methods of fabrication

PTO-2040  
12/99

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg. 	Figs. Drwg. 21	Print Fig. 2B	Total Claims 49
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	SHEILA V. CLARK EXAMINER GROUP 2800  (Primary Examiner) (Date)		12-1-4	
			<b>ISSUE FEE</b>	
<input type="checkbox"/> c) The terminal _____ months of this patent have been disclaimed.	 (Legal Instruments Examiner) (Date)		Amount Due	Date Paid
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~~CONFIDENTIAL~~ Formal Drawings (b)(1) set

(FACE)